

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki et al.                      Art Unit : 2815  
Serial No. : 10/693,491                                      Examiner : N. Drew Richards  
Filed : October 27, 2003                                      Conf. No. : 3131  
Title : SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREOF

**MAIL STOP RCE**

Commissioner for Patents  
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INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: 7/12/06

  
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Substitute Form PTO-1449 (Modified)  <b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)  (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-097002	Application No. 10/693,491
	Applicant Shunpei Yamazaki et al.		
	Filing Date October 27, 2003	Group Art Unit 2815	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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Foreign Patent Documents or Published Foreign Patent Applications								
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							Yes	No
	AP							
	AQ							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AR	
	AS	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	